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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

Complete If Known

Application Number	10/747,975
Filing Date	12/29/03
First Named Inventor	Jorge L. Salcedo
Art Unit	2829
Examiner Name	TANG
Attorney Docket Number	TI-36516

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Mark Tama

Date Considered

12/28/04

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Art Unit	2829
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Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MT		JORGE SALCEDO-SUNER, "A New Induced Latchup Phenomenon Generated By Overvoltage Stress In Voltage Tolerant ESD Protection Circuits", Texas Instruments, Inc, (date unknown)	

Examiner Signature		Date Considered	12/28/04
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